

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Phan	
Application No.: 10/771811	Group Art Unit: 2111
Filed: 2/4/2004	
Title: Highly Available Dual Serial Bus Architecture	Examiner: Phan
Attorney Docket No.:	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE AFTER FINAL**

Dear Sir:

In response to the Final Office Action of July 25, 2006, entry of this response pursuant to 37 C.F.R. §1.116(e) is respectfully requested. Changes to the specification begin on page 2 of this paper. A list of claims begins on page 3 of this paper. Remarks begin on page 5 of this paper.